

Prof. Hisa Ando

Present Address: ZIP 233-0006, 1-11-5-101,
Serigaya, Kohnan-ku,
Yokohama Japan.

Course Name: Advanced Computer Architecture (EM 601b)

Period of stay: She will arrive to Jabalpur by SG2453 at 8:45 AM on Feb. 14th. She will leave Jabalpur by SG2452 at 12:20 on Feb. 24th.



Brief Biography:

She has been writing high performance computing related technical articles to Web media in Japan. At present she is retired and has no affiliation. She got her BSEE and MSEE from Tokyo Tech. She also earned her Ph.D from Tokyo Tech in 2006. She wrote 4 computer architecture books. She also translated “The datacenter as a computer: An introduction to the design of Warehouse Scale Machines”. Second Edition” written by Barroso et. al. to Japanese.

Details about course:

She would cover the hardware design of high performance computer in this course. Course contents include basic concepts of pipeline, cache and arithmetic units. It covers how those units are designed in detail, for example how to control pipeline. It also teaches memory management, out-of-order execution, branch prediction, multi-processor system, network for connecting multiple processors to build large computer system.

